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FIG. 1

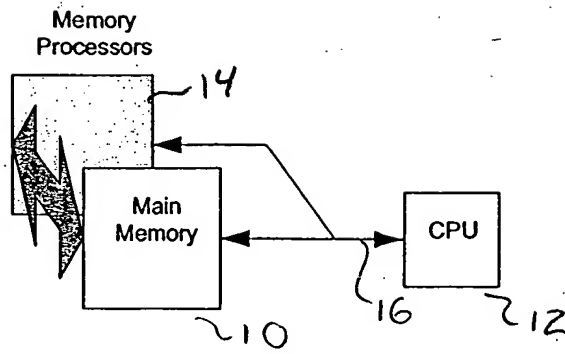
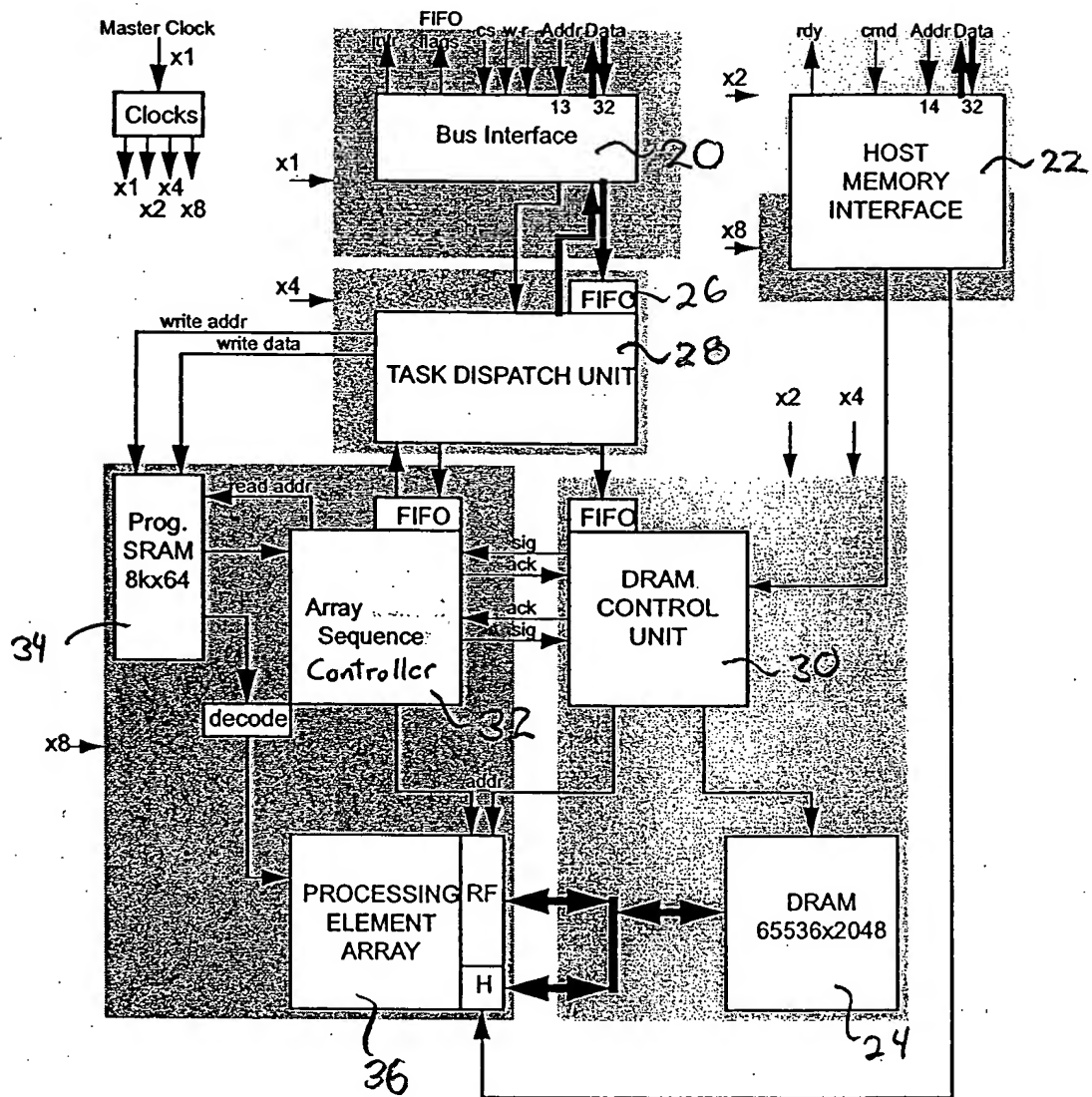


FIG. 2



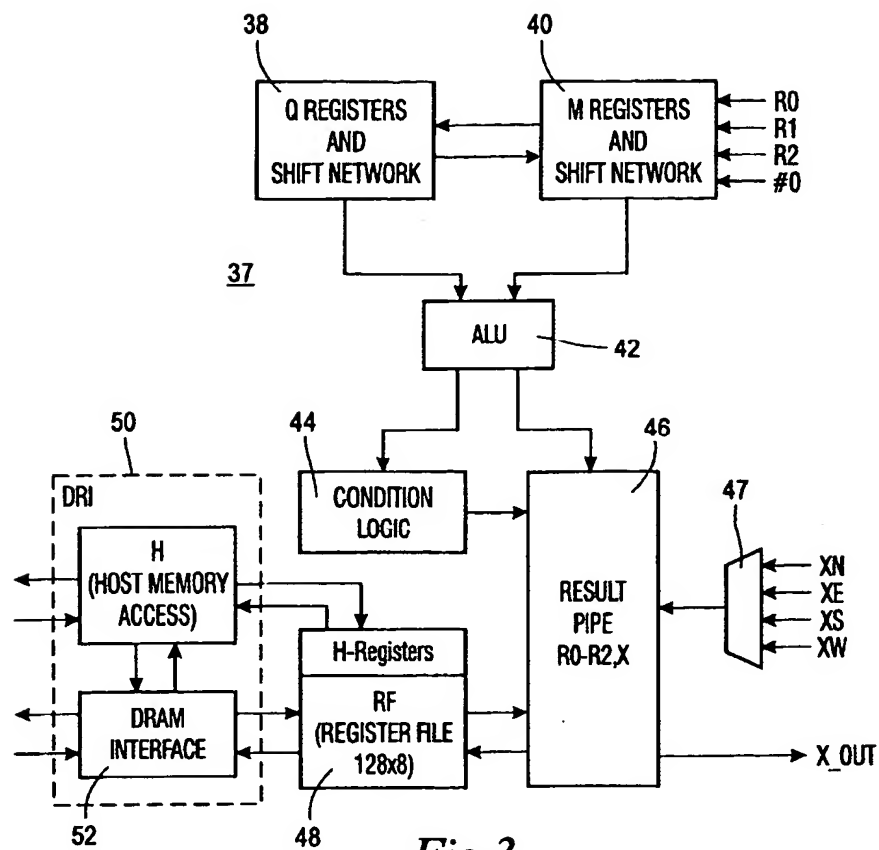


Fig.3

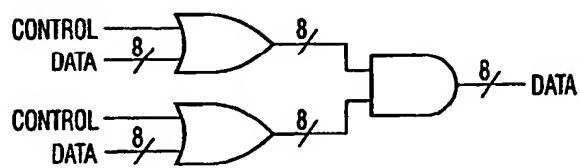
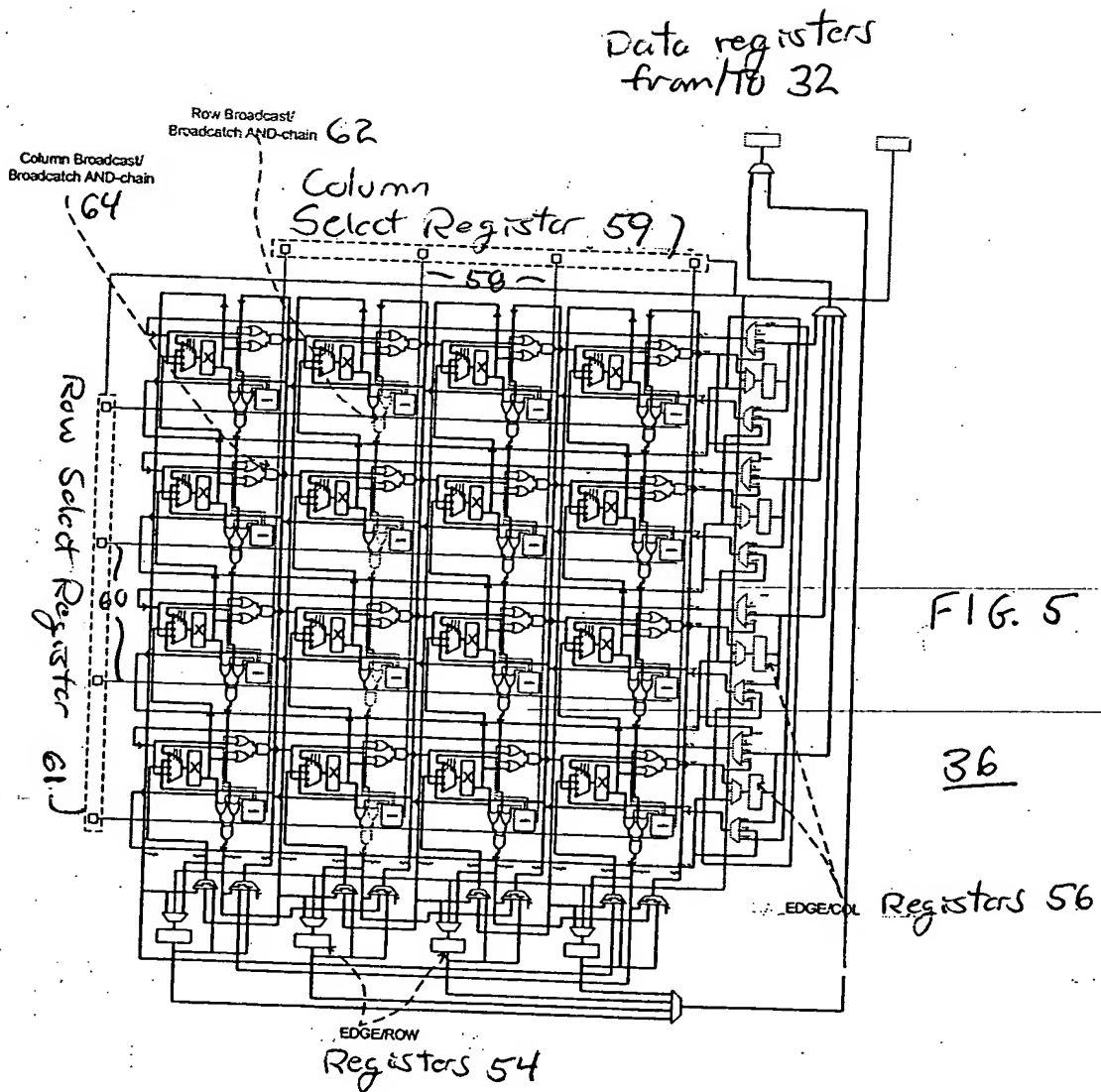
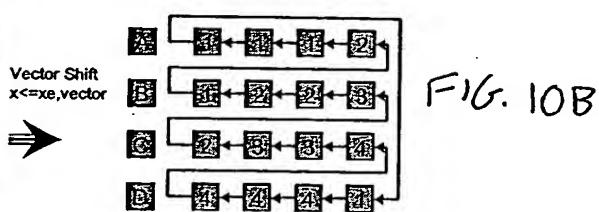
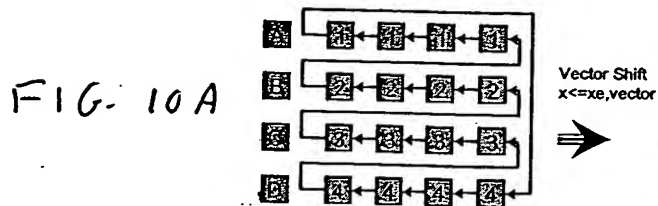
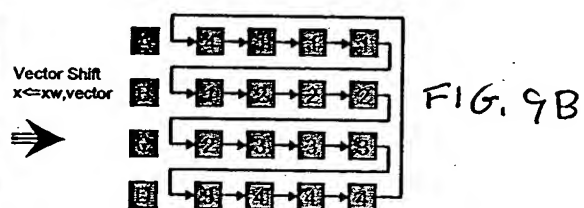
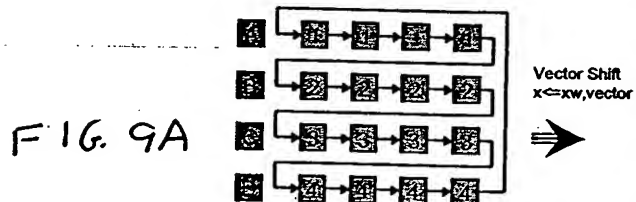
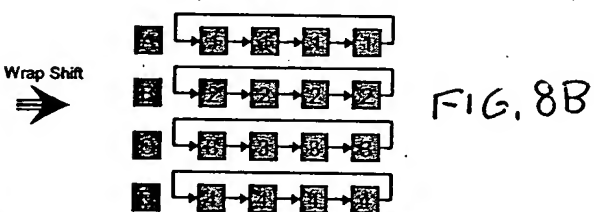
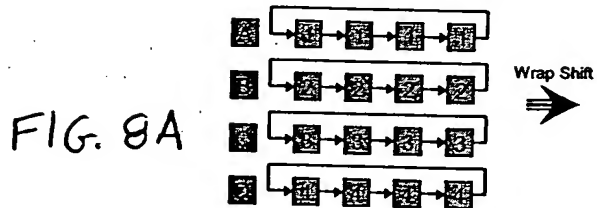
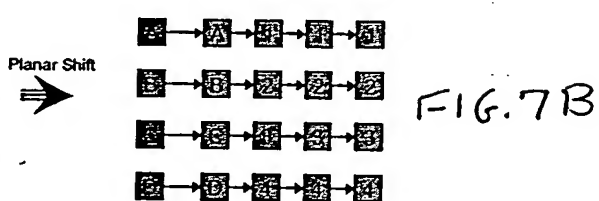
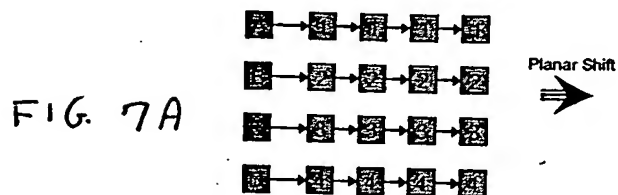
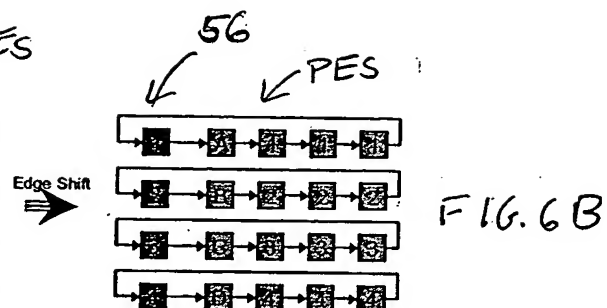
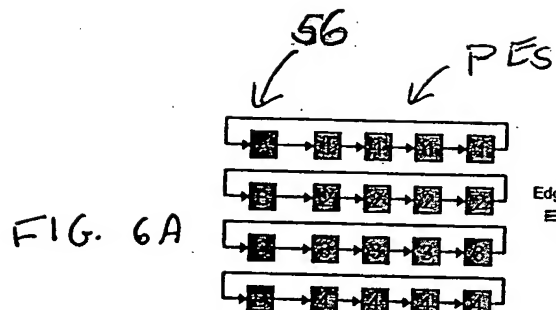


Fig.4





Data is broadcast from edge registers

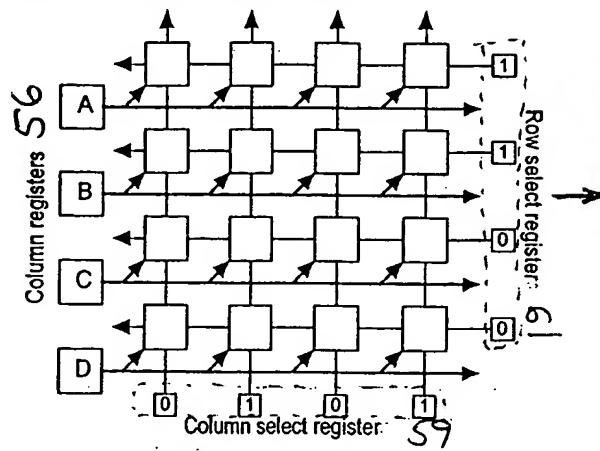


FIG. 11A

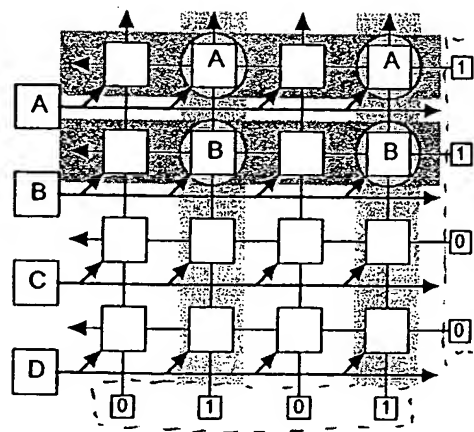


FIG. 11B

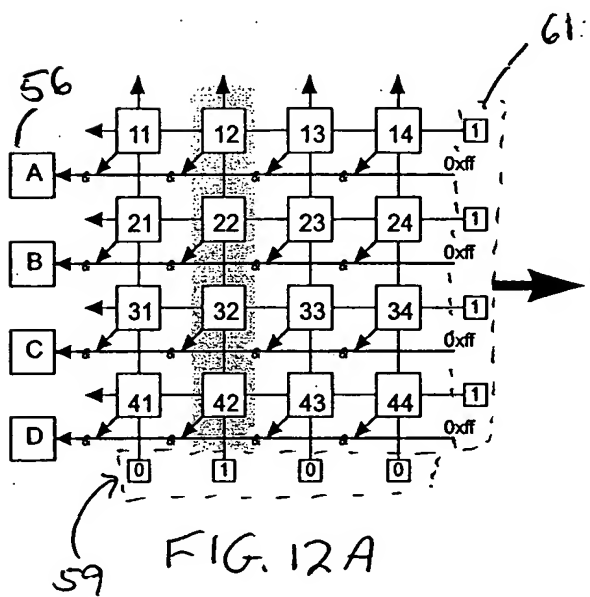


FIG. 12A

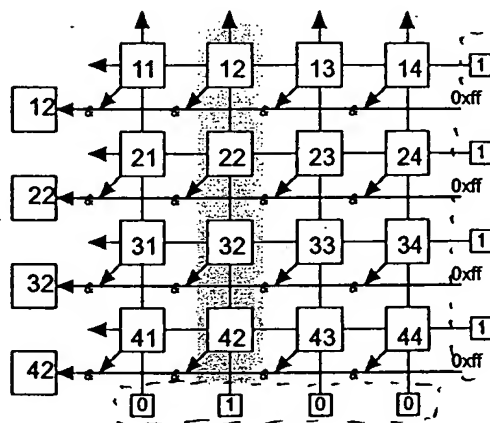


FIG. 12B

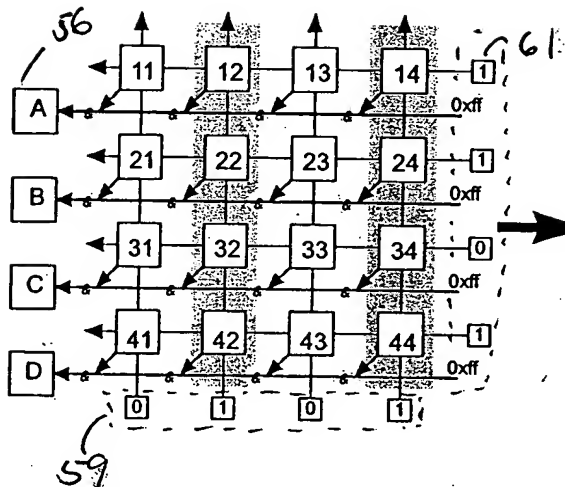


FIG. 13A

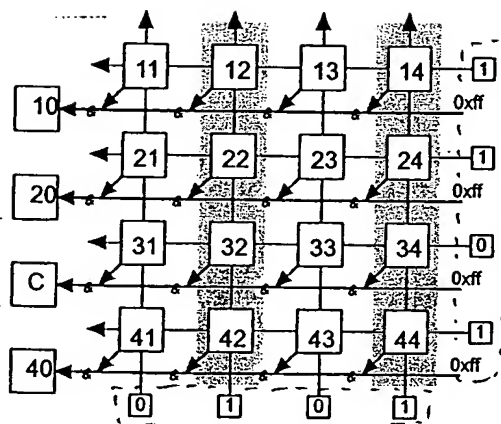


FIG. 13B

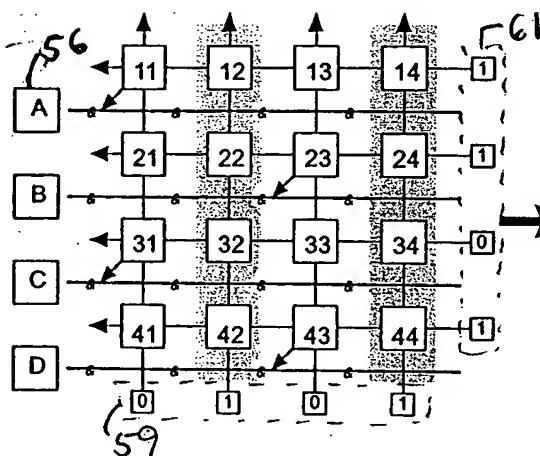


FIG. 14A

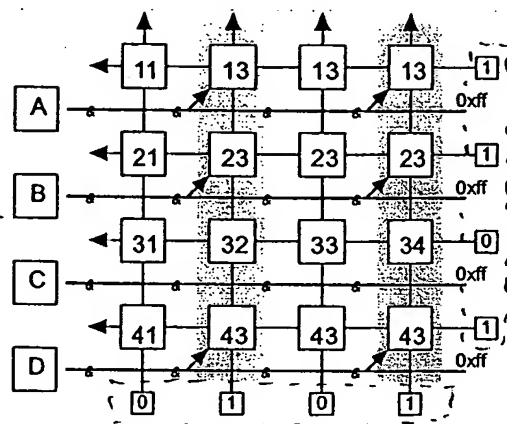


FIG. 14B

	C7	C5	C3	C1				
	C6	C4	C2	C0				
r0	A0	A1	A2	A3	A4	A5	A6	A7
r1	B0							B7
r2	C0							C7
r3	D0							D7
r4	E0							E7
r5	F0							F7
r6	G0							G7
r7	H0	H1	H2	H3	H4	H5	H6	H7

FIG. 15A

A0	A1	A2	A3	A4	A5	A6	A7
B7	B0					B6	B7
C6	C7	C0				C5	C7
D5	D7	D0				D4	D7
E4		E7	E0			E3	
F3			F7	F0		F2	
G2				G7	G0	G1	
H1	H2	H3	H4	H5	H6	H7	H0

FIG. 15B

A7	B7	C7	D7	E7	F7	G7	H7
A6	B6	C6					H6
A5		C5	D5				H5
A4			D4	E4			H4
A3				E3	F3		H3
A2					F2	G2	H2
A1						G1	H1
A0	B0	C0	D0	E0	F0	G0	H0

FIG. 15D

B7	C7	D7	E7	F7	G7	H7	A7
C6					H6	A6	B6
D5				H5	A5		C5
E4			H4	A4			D4
F3		H3	A3				E3
G2	H2	A2					F2
H1	A1						G1
A0	B0	C0	D0	E0	F0	G0	H0

FIG. 15C

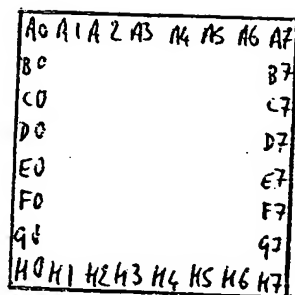


FIG. 16A

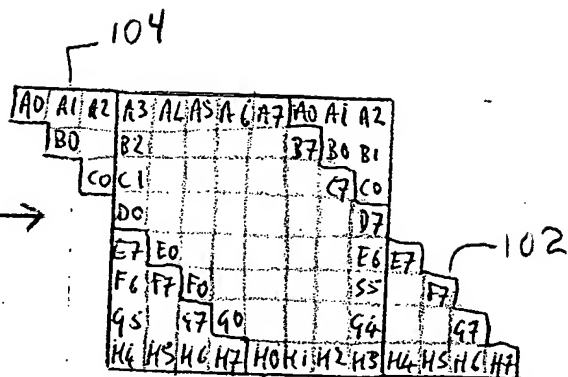


FIG. 16B

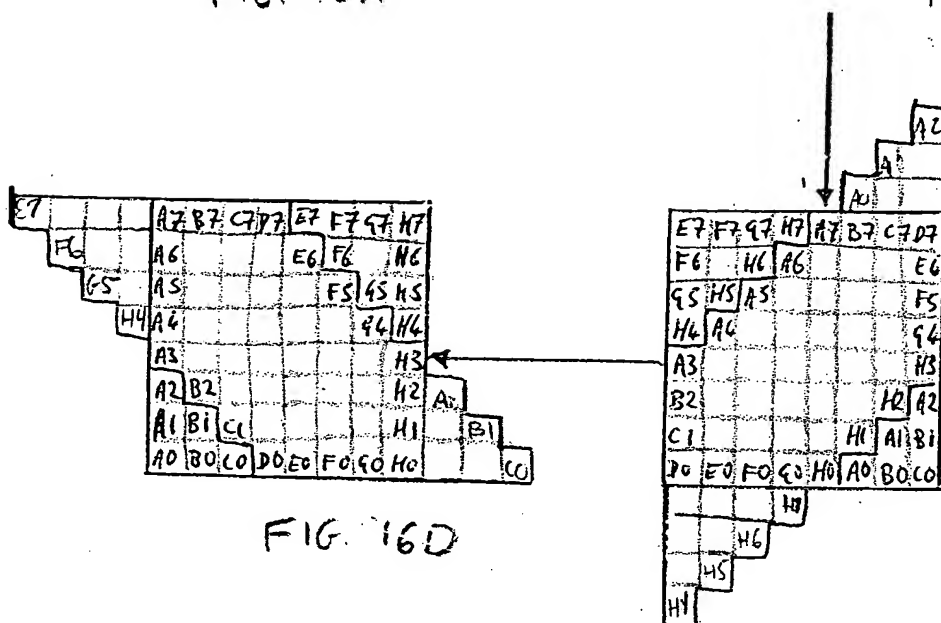


FIG. 16C

FIG. 16D